Inventor Name Search Result

Your Search was:

Last Name = LOVE
First Name = SIMON

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07084952	4870561	150	07/20/1987	USER INTERFACE SIMULATION AND MANAGEMENT FOR PROGRAM-CONTROLLED APPARATUS	LOVE, SIMON
09549336	Not Issued	071	04/14/2000	SERVICE DETAIL RECORDS FOR LAYERED COMMUNICATIONS PROTOCOLS	LOVE, SIMON
09703542	Not Issued	094	11/01/2000	SYSTEM AND METHOD FOR MONITORING COMMUNICATION NETWORKS USING DATA STREAM CHARACTERIZATION	LOVE, SIMON
09704002	6738355	150	11/01/2000	SYNCHRONIZATION METHOD FOR MULTI-PROBE COMMUNICATIONS NETWORK MONITORING	LOVE, SIMON
09704051	6831890	150	10/31/2000	MEASURING NETWORK PERFORMANCE PARAMETERS IN DATA COMMUNICATION NETWORKS	LOVE, SIMON
09735607	Not Issued	071	12/14/2000	MEASURING EFFICIENCY OF DATA TRANSMISSION	LOVE, SIMON
10726439	Not Issued	030	12/03/2003	COMPENSATED REFRESH OSCILLATOR	LOVETT, SIMON
08958464	6097222	150	10/27/1997	SYMMETRICAL NOR GATES	LOVETT, SIMON J
09521190	6292403	150	03/07/2000	CIRCUIT AND METHOD FOR IMPLEMENTING SINGLE-CYCLE READ/WRITE OPERATION(S), AND RANDOM ACCESS MEMORY INCLUDING THE CIRCUIT AND/OR PRACTICING THE METHOD	LOVETT, SIMON J.
09735526	6664799	150	12/14/2000	METHOD AND APPARATUS FOR ENABLING A DIGITAL MEMORY TESTER TO READ THE FREQUENCY OF A FREE RUNNING OSCILLATOR	LOVETT, SIMON J.
09878433	6385128	150	06/11/2001	RANDOM ACCESS MEMORY HAVING A READ/WRITE ADDRESS BUS AND PROCESS FOR WRITING TO AND READING FROM THE SAME	LOVETT, SIMON J.
09878434	6445645	150	06/11/2001	RANDOM ACCESS MEMORY HAVING INDEPENDENT READ PORT AND	LOVETT, SIMON J.

				WRITE PORT AND PROCESS FOR WRITING TO AND READING FROM THE SAME	
09915823	6538466	150	07/26/2001	BUFFER WITH STABLE TRIP POINT	LOVETT, SIMON J.
09942898	Not Issued	092	08/30/2001	ZERO POWER CHIP STANDBY MODE	LOVETT, SIMON J.
10102221	6690606	150	03/19/2002	ASYNCHRONOUS INTERFACE CIRCUIT AND METHOD FOR A PSEUDO-STATIC MEMORY DEVICE	LOVETT, SIMON J.
10225423	6750497	150	08/22/2002	HIGH-SPEED TRANSPARENT REFRESH DRAM-BASED MEMORY CELL	LOVETT, SIMON J.
10228932	6879538	150	08/28/2002	ZERO POWER CHIP STANDBY MODE	LOVETT, SIMON J.
10230046	6845054	150	08/28/2002	ZERO POWER CHIP STANDBY MODE	LOVETT, SIMON J.
10232935	Not Issued	094	08/28/2002	ZERO POWER CHIP STANDBY MODE	LOVETT, SIMON J.
10357862	Not Issued	092	02/03/2003	DETECTION CIRCUIT FOR MIXED ASYNCHRONOUS AND SYNCHRONOUS MEMORY OPERATION	LOVETT, SIMON J.
10436166	6839297	150	05/13/2003	HIGH-SPEED TRANSPARENT REFRESH DRAM-BASED MEMORY CELL	LOVETT, SIMON J.
10613236	Not Issued	030	07/03/2003	TEMPERATURE SENSING DEVICE IN AN INTEGRATED CIRCUIT	LOVETT, SIMON J.
10754658	Not Issued	093	01/08/2004	ASYNCHRONOUS INTERFACE CIRCUIT AND METHOD FOR A PSEUDO-STATIC MEMORY DEVICE	LOVETT, SIMON J.
10873968	Not Issued	030	06/22/2004	APPARATUS AND METHOD FOR IMPROVING DYNAMIC REFRESH IN A MEMORY DEVICE	LOVETT, SIMON J.
10969184	Not Issued	030	10/21/2004	METHOD OF OPERATING A MEMORY CELL	LOVETT, SIMON J.
08958500	5889416	150	10/27/1997	SYMMETRICAL NAND GATES	LOVETT, SIMON J.
09021461	6278295	150	02/10/1998	BUFFER WITH STABLE TRIP POINT	LOVETT, SIMON J.
09238270	6069839	150	01/27/1999	CIRCUIT AND METHOD FOR IMPLEMENTING SINGLE-CYCLE READ/WRITE OPERATION(S), AND RANDOM ACCESS MEMORY INCLUDING THE CIRCUIT AND/OR PRACTICING THE METHOD	LOVETT, SIMON J.
09238953	6262936	150	01/27/1999	RANDOM ACCESS MEMORY HAVING INDEPENDENT READ PORT AND WRITE PORT AND PROCESS FOR WRITING TO AND READING FROM	LOVETT, SIMON J.

				THE SAME	
09238954	6262937	150	01/27/1999	RANDOM ACCESS MEMORY HAVING A READ/WRITE ADDRESS BUS AND PROCESS FOR WRITING TO AND READING FROM THE SAME	LOVETT, SIMON J.
09277616	6100560	150	03/26/1999	NONVOLATILE CELL	LOVETT, SIMON J.
09458776	6181621	150	12/10/1999	THRESHOLD VOLTAGE MISMATCH COMPENSATED SENSE AMPLIFIER FOR SRAM MEMORY ARRAYS	LOVETT, SIMON J.
60077982	Not Issued	159	03/13/1998	RANDOM ACESS MEMORY AND PROCESS FOR WRITING TO AND READING FROM THE SAME	LOVETT, SIMON J.
60078029	Not Issued	159	03/13/1998	RANDOM ACCESS MEMORY HAVING A READ/WRITE ADDRESS BUS AND PROCESS FOR WRITING TO AND READING FROM THE SAME	LOVETT, SIMON J.
60078718	Not Issued	159	03/20/1998	CIRCUIT AND METHOD FOR IMPLEMENTING SINGLE-CYCLE READ/WRITE OPERATION(S), AND RANDOM ACCESS MEMORY INCLUDING THE CIRCUIT AND/OR PRACTICING THE METHOD	LOVETT, SIMON J.
08774293	5936973	150	12/23/1996	TEST MODE LATCHING SCHEME	LOVETT, SIMON JOHN

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First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09735607	Not Issued	071	12/14/2000	MEASURING EFFICIENCY OF DATA TRANSMISSION	ARCHIBALD, DAVID
09679078	Not Issued	041	10/05/2000	CORRELATION OF SIGNALLING MESSAGES	ARCHIBALD, DAVID MOIR
09834046	Not Issued	164	04/12/2001	GENERATING CALL DETAIL RECORDS	ARCHIBALD, DAVID MOIR
10151674	Not Issued	030	05/20/2002	SYSTEM AND METHOD FOR CORRELATION AND REAL-TIME DISPLAY OF MAPPED PSTN TRUNK IDENTIFIERS AND GATEWAY CONTROL PROTOCOL ENDPOINTS	ARCHIBALD, DAVID. MOIR
10222994	Not Issued	030	08/19/2002	GENERATING CALL DETAIL RECORDS	ARCHIBALD, DAVID MOIR
09367396	Not Issued	041	08/13/1999	MONITORING ISDN LINKS	ARCHIBALD, DAVID MOIR
09451893	Not Issued	061	12/01/1999	MONITORING LINKS IN ATM NETWORKS	ARCHIBALD, DAVID MOIR
06843290	4662642	250	03/24/1986	ROPING DUMMY FOR PRACTICING ROPING OF CALVES	ARCHIBALD, DAVID R.
06764054	4645191	150	08/09/1985	VARIABLE SPEED CONVEYOR APPARATUS	ARCHIBALD, DAVID W

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